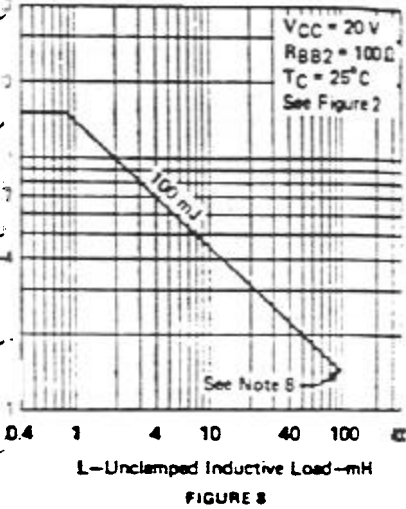


# POWER TRANSISTORS

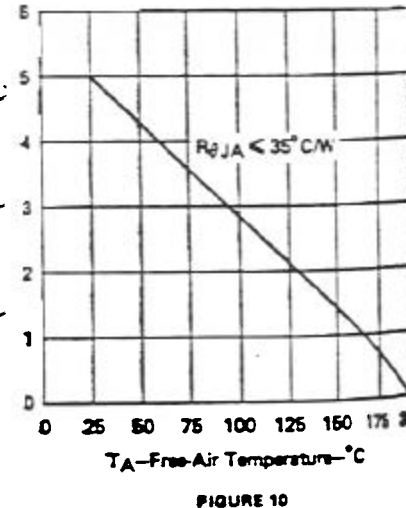
## LOADING AREAS

MAXIMUM COLLECTOR CURRENT  
vs  
UNCLAMPED INDUCTIVE LOAD



## DERATING

FREE-AIR TEMPERATURE  
DISSIPATION DERATING CURVE



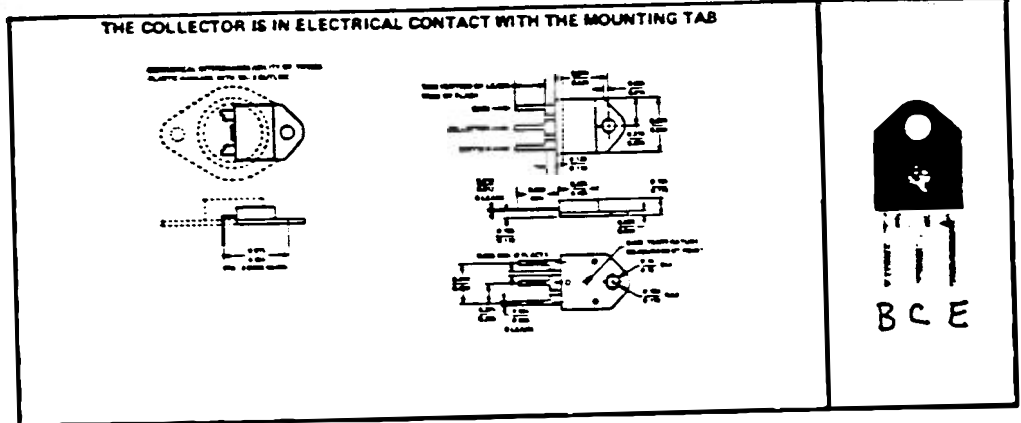
# TYPE TIP29E

# P-N-P SINGLE-DIFFUSED MESA SILICON POWER TRANSISTOR

FOR POWER AMPLIFIER AND HIGH-SPEED SWITCHING APPLICATIONS  
RECOMMENDED FOR COMPLEMENTARY USE WITH TIP3055

- 90 Watts at 25°C Case Temperature
- 15 A Rated Collector Current
- 62.5 mJ Reverse Energy Rating

## Mechanical Data



Absolute maximum ratings at 25°C case temperature (unless otherwise noted)

Collector-Base Voltage	.....	-100 V
Collector-Emitter Voltage (See Note 1)	.....	-70 V
Emitter-Base Voltage	.....	-7 V
Continuous Collector Current	.....	-15 A
Continuous Base Current	.....	-7 A
Safe Operating Region at (or below) 25°C Case Temperature	.....	See Figure 5
Continuous Device Dissipation at (or below) 25°C Case Temperature (See Note 2)	.....	90 W
Continuous Device Dissipation at (or below) 25°C Free-Air Temperature (See Note 3)	.....	3.5 W
Unclamped Inductive Load Energy (See Note 4)	.....	62.5 mJ
Operating Collector Junction Temperature Range	.....	-65°C to 150°C
Storage Temperature Range	.....	-65°C to 150°C
Lead Temperature 1/8 Inch from Case For 10 Seconds	.....	260°C

- NOTES
1. This value applies when the base-emitter resistance  $R_{BE} = 100 \Omega$ .
  2. Derates linearly to 150°C case temperature at the rate of 0.72 W/°C.
  3. Derates linearly to 150°C free-air temperature at the rate of 28 mW/°C.
  4. This rating is based on the capability of the transistor to operate safely in the circuit of Figure 2.  $L = 20 \text{ mH}$ ,  $R_{BB2} = 100 \Omega$ ,  $V_{BB2} = 0 \text{ V}$ ,  $R_B = 0.1 \Omega$ ,  $V_{CC} = 10 \text{ V}$ , Energy =  $I_C^2 L/2$ .

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# TYPE TIP2955

## P-N-P SINGLE-DIFFUSED MESA SILICON POWER TRANSISTOR

electrical characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS	MIN	MAX
$V_{(BR)CEO}$ Collector-Emitter Breakdown Voltage	$I_C = -30 \text{ mA}$ , $I_B = 0$ , See Note 5	-50	V
$I_{CEB}$ Collector Cutoff Current	$V_{CE} = -70 \text{ V}$ , $R_{BE} = 100 \Omega$	-1	mA
$I_{CEO}$ Collector Cutoff Current	$V_{CE} = -30 \text{ V}$ , $I_B = 0$	-2.5	mA
$I_{CEV}$ Collector Cutoff Current	$V_{CE} = -100 \text{ V}$ , $V_{BE} = 1.5 \text{ V}$	-4	mA
$I_{EB0}$ Emitter Cutoff Current	$V_{EB} = -7 \text{ V}$ , $I_C = 0$	-4	mA
$h_{FE}$ Static Forward Current Transfer Ratio	$V_{CE} = -4 \text{ V}$ , $I_C = -4 \text{ A}$ , See Notes 5 and 6	20	%
$V_{BE}$ Base-Emitter Voltage	$V_{CE} = -4 \text{ V}$ , $I_C = -10 \text{ A}$ , See Notes 5 and 6	5	V
$V_{CE(sat)}$ Collector-Emitter Saturation Voltage	$I_B = -400 \text{ mA}$ , $I_C = -4 \text{ A}$ , See Notes 5 and 6	-1.1	V
	$I_B = -3.3 \text{ A}$ , $I_C = -10 \text{ A}$ , See Notes 5 and 6	-5	V
$h_{fe}$ Small-Signal Common-Emitter Forward Current Transfer Ratio	$V_{CE} = -4 \text{ V}$ , $I_C = -1 \text{ A}$ , $f = 1 \text{ kHz}$	15	
$f_{th}$ Small-Signal Common-Emitter Forward Current Transfer Ratio Cutoff Frequency	$V_{CE} = -4 \text{ V}$ , $I_C = -1 \text{ A}$ , See Note 7	10	MHz

- NOTES
- These parameters must be measured using pulse techniques,  $t_w = 300 \mu\text{s}$ , duty cycle  $\leq 2\%$ .
  - These parameters are measured with voltage-sensing contacts separate from the current-carrying contacts and located within 0.1 inch from the device body.
  - $f_{th}$  is the frequency at which the magnitude of the small-signal forward current transfer ratio is 0.707 of its low-frequency value. For this device, the reference measurement is made at 1 kHz.

thermal characteristics

PARAMETER	MAX	UNIT
$R_{\theta JC}$ Junction-to-Case Thermal Resistance	1.39	$^{\circ}\text{C/W}$
$R_{\theta JA}$ Junction-to-Free-Air Thermal Resistance	35.7	$^{\circ}\text{C/W}$

switching characteristics at 25°C case temperature

PARAMETER	TEST CONDITIONS <sup>1</sup>	Typ	UNIT
$t_{on}$ Turn-On Time	$I_C = -6 \text{ A}$ , $I_{B(1)} = -0.6 \text{ A}$ , $I_{B(2)} = 0.6 \text{ A}$ ,	0.4	ns
$t_{off}$ Turn-Off Time	$V_{BE(off)} = 4 \text{ V}$ , $R_L = 5 \Omega$ , See Figure 1	0.7	ns

<sup>1</sup>Voltage and current values shown are nominal; exact values vary slightly with transistor parameters.

# TRANSISTOR

CONDITIONS	MIN	MAX	UNIT
$V_{CE}$ See Note 5	-60		V
$I_C = 100 \mu A$		-1	mA
$V_{CE} = 1.5 V$		-0.7	mA
$I_C = 0$		-5	mA
$I_C = -4 A$ See Notes 5 and 6	20	70	
$I_C = -10 A$ See Notes 5 and 6	5		
$V_{CE} = -4 A$ See Notes 5 and 6		-1.8	V
$V_{CE} = -4 A$ See Notes 5 and 6		-1.1	V
$I_C = -10 A$ See Notes 5 and 6		-3	
$I_C = -1 A, f = 1 kHz$		15	
$I_C = -1 A$ See Note 7		10	kHz

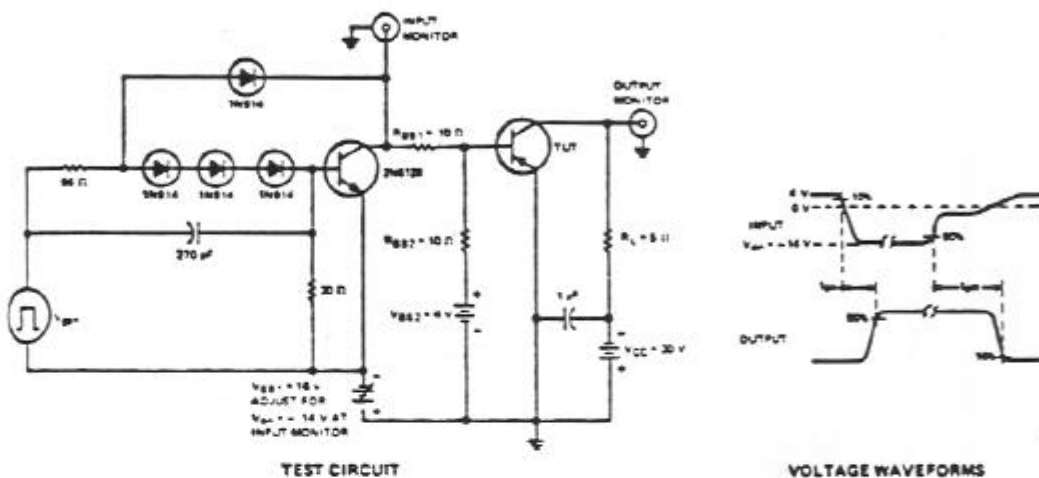
$\beta_{DC} < 2\%$   
 current-carrying contacts and located within 0.125  
 transfer ratio is 0.707 of its low-frequency value

	MAX	UNIT
	1.39	$^{\circ}C/W$
	35.7	

CONDITIONS†	TYP	UNIT
$I_C = -0.5 A, I_B(I_C) = 0.5 A$	0.4	$\mu s$
$R_L = 5 \Omega$ See Figure 1	0.7	

## TYPE TIP2955 P-N-P SINGLE-DIFFUSED MESA SILICON POWER TRANSISTORS

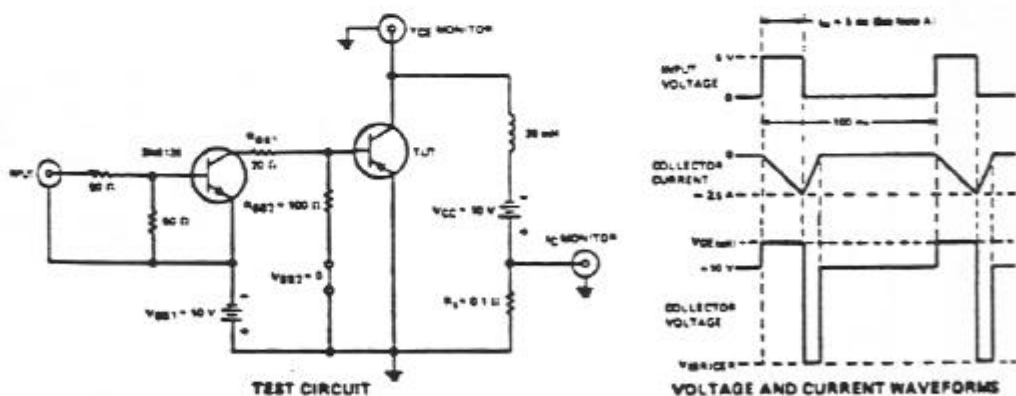
### PARAMETER MEASUREMENT INFORMATION



- NOTES  
 A.  $V_{BE1}$  is a 30-V pulse (from 0 V) into a 50- $\Omega$  termination.  
 B. The  $V_{BE2}$  waveform is supplied by a generator with the following characteristics:  $t_r < 15 ns$ ,  $t_f < 15 ns$ ,  $Z_{OUT} = 50 \Omega$ ,  $t_{WH} = 20 \mu s$ , duty cycle  $< 2\%$ .  
 C. Waveforms are monitored on an oscilloscope with the following characteristics:  $t_r < 15 ns$ ,  $R_{IN} > 10 M\Omega$ ,  $C_{IN} < 11.6 pF$ .  
 D. Resistors must be noninductive types.  
 E. The d-c power supplies may require additional bypassing in order to minimize ringing.

FIGURE 1

### INDUCTIVE LOAD SWITCHING



NOTE A: Input pulse width is increased until  $I_{CM} = -2.5 A$ .

FIGURE 2

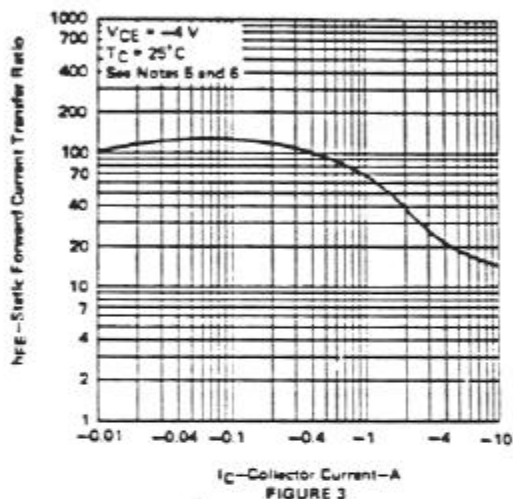
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# TYPE TIP2955

## P-N-P SINGLE-DIFFUSED MESA SILICON POWER TRANSISTOR

### TYPICAL CHARACTERISTICS

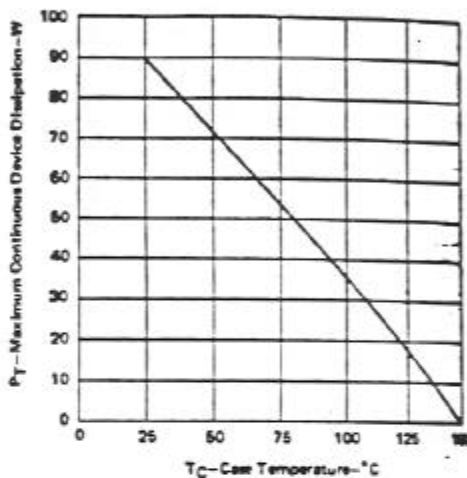
STATIC FORWARD CURRENT TRANSFER RATIO  
vs  
COLLECTOR CURRENT



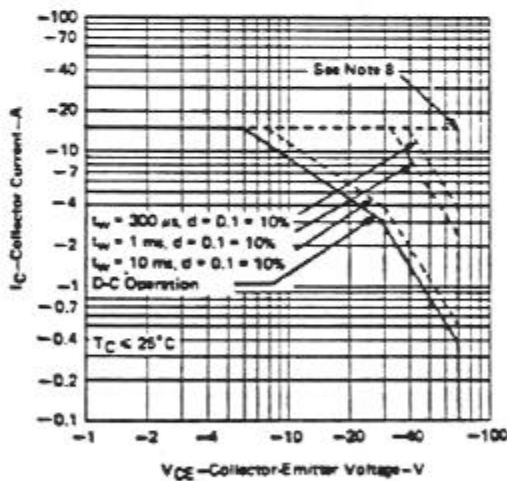
- NOTES
- These parameters must be measured using pulse techniques:  $t_w = 300 \mu s$ , duty cycle  $\leq 2\%$ .
  - These parameters are measured with voltage-sensing contacts separate from the current-carrying contacts and located within 0.125 inch from the device body.

### THERMAL INFORMATION

DISSIPATION DERATING CURVE



### MAXIMUM SAFE OPERATING REGION



NOTE 8: This combination of maximum voltage and current may be achieved only when switching from saturation to cutoff with a storage inductive load.

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