



Rockwell

R6500 Microprocessor Programming Reference Card

R6500/1 ONE-CHIP MICROCOMPUTER

R6500/1 MEMORY MAP



Address Range	Function	HEX	Category
FFF	IRQ Vector High	FFF	ROM
FFE	IRQ Vector Low	FFE	
FFD	RES Vector High	FFD	
FFC	RES Vector Low	FFC	
FFB	NMI Vector High	FFB	
FFA	NMI Vector Low	FFA	
800 - 7FF	User Program	800 - 7FF	ROM
400	R6500/1E User Program (1)	400	
	Unassigned		Input/Output
08F	Control Register	08F	
08E	Unassigned	08E	
08B	Unassigned	08B	
08A	Clear PA1 Neg Edge Detected (2)	08A	
089	Clear PA0 Pos Edge Detected (2)	089	
088	Upper Latch and Transfer Latch to Counter (3)	088	
087	Lower Count (3)	087	
086	Upper Count	086	
085	Lower Latch	085	
084	Upper Latch	084	
083	PORT D	083	
082	PORT C	082	
081	PORT B	081	
080	PORT A	080	
	Unassigned		RAM
03F - 000	User RAM	03F - 000	

Notes:

- (1) Additional 1024 bytes are decoded for external memory addressing by R6500/1E Emulator Device.
- (2) I/O command only; i.e., no stored data.
- (3) Clears Counter Overflow — Bit 7 in Control Register.

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