

CAV 2008

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20th International Conference on
Computer Aided Verification

July 7–14 **PRINCETON
UNIVERSITY**
Princeton . New Jersey . USA

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South facade of Nassau Hall (1756), Princeton University; photo by Denise Applewhite

INVITED SPEAKERS

Edward Felten *Princeton University*
James Larus *Microsoft Research*

INVITED TUTORIALS

Harry Foster *Mentor Graphics*
John Harrison *Intel*
Peter O'Hearn *University of London*
Reinhard Wilhelm *Saarland University*

AFFILIATED EVENTS

CAV Award
HW-MC-Comp
SMT-Comp

WORKSHOPS July 7, 8, and 14

AFM: Automated Formal Methods
BPR: Bit Precise Reasoning
(EC)^2: Exploiting Concurrency Efficiently and Correctly
FAC: Formal verification of Analog Circuits
HAV: Heap Analysis and Verification
NSV: Numerical abstractions for Software Verification
SMT: Satisfiability Modulo Theories

CONFERENCE CHAIRS

Aarti Gupta *NEC Labs America*
Sharad Malik *Princeton University*

WORKSHOPS CHAIR

Byron Cook *Microsoft Research*

TUTORIALS CHAIR

Alan Hu *University of British Columbia*

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Corina Pasareanu *NASA*
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