Nanoscale Tera-Hertz Metal-Semiconductor-Metal Photodetectors

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Abstract—Metal-semiconductor-metal photodetectors (MSM PD’s) with finger spacing and width as small as 25 nm were fabricated on bulk and low-temperature (LT) grown GaAs and crystalline Si using ultrahigh resolution electron-beam lithography. High-speed electrooptic characterization with a 100 fs pulsed laser showed that the fastest MSM PD’s had finger spacing and width, full width at half maximum response time, and 3 dB bandwidth, respectively, of 300 nm, 0.87 ps, and 0.51 THz for LT-GaAs; 100 nm, 1.5 ps, and 0.3 THz for bulk GaAs; and 100 nm, 10.7 ps, and 41 GHz for crystalline Si. To our knowledge, these detectors are the fastest nanoscale MSM PD’s on each of these materials reported to date. Monte Carlo simulation was used to understand the impulse response of the MSM PD’s and to explore the ultimate speed limitation of transit-time-limited MSM PD’s on GaAs and Si. Factors that are important to detector capacitance were identified using a conformal mapping method. Based on the experimental data, Monte Carlo simulation, and calculation of detector capacitance, scaling rules for achieving high-speed MSM PD’s are presented.

I. INTRODUCTION

Metal-semiconductor-metal (MSM) photodetectors (MSM PD’s) are very attractive for many optoelectronic applications, such as optical communication, future high-speed chip-to-chip connection, and high-speed sampling, because of their high sensitivity-bandwidth product and their compatibility with large-scale planar integrated circuit (IC) technology. A MSM PD consists of interdigitated metal fingers on a semiconductor, and it detects photons by collecting electric signals generated by photoexcited electrons and holes in the semiconductor that drift under the electrical field applied between the fingers (Fig. 1). MSM PD’s can be classified according to whether their speed is intrinsically limited by the carrier transit time between the fingers or the carrier recombination time. Certainly, the speed of the detector will be limited by the $RC$ time constant if it is longer than the transit time or the recombination time. Usually, transit-time-limited detectors are fabricated on high-quality semiconductors and have a sensitivity several orders of magnitude higher than that of recombination-time-limited MSM PD’s. Furthermore, their fabrication technology is very compatible with field effect transistor (FET) fabrication. In the past, however, transit-time-limited MSM PD’s generally were much slower than recombination-time-limited MSM PD’s, because it used to be difficult to make the finger spacing small. In recombination-time-limited MSM PD’s, shorter recombination time and therefore higher speed are achieved through introducing high-density recombination centers into the semiconductor. This drastically lowers sensitivity and makes fabrication less compatible with FET IC fabrication.

As fabrication technology advances, the finger spacing of a MSM PD can be reduced to nanometer scale. The smaller the finger spacing, the shorter the intrinsic response time for the transit-time-limited MSM PD’s and the higher the sensitivity for the recombination-time-limited MSM PD’s. Therefore, the difference in speed and sensitivity between the transit-time-limited and recombination-time-limited MSM PD’s is getting smaller. As shown later, the finger width is another important factor for high-speed MSM PD’s. The narrower the finger width, the less the detector capacitance per unit finger length and the shorter the external response time.

The study of MSM structures emerged in early 1970’s [1], [2]. In 1975, recombination-time-limited MSM PD’s on Cr doped GaAsCr substrates achieved an impulse response of 92 ps [3]. In the early 1980’s, MSM PD’s on amorphous silicon showed impulse response of a few pi-
coseconds [4], [5]. Then, recombination-time-limited single-gap MSM PD’s on radiation damaged silicon-on-sapphire exhibited a carrier lifetime in the subpicosecond region and was widely used for high-speed sampling [6], [7]. Much progress has been made since then, due to the development of novel semiconductor growth, fabrication techniques, and the availability of femtosecond lasers. Both transit-time-limited and recombination-time-limited MSM PD’s have been fabricated on many substrates such as GaAs [8]–[17], InGaAs [18]–[22], polycrystalline GaAs [23], LT-GaAs [24], [25], amorphous Si [4], [26]–[29], and crystalline Si [30], [31]. The fastest transit-time-limited GaAs MSM PD reported previously has a finger width of 0.75 μm and finger spacing of 0.5 μm, an impulse response of 4.8 ps full width at half maximum (FWHM), and a 3 dB bandwidth of 105 GHz [12]. The fastest recombination-time-limited MSM PD on LT-GaAs reported previously had a finger width and spacing of 0.2 μm, a FWHM of 1.2 ps, and a 3 dB bandwidth of 375 GHz [24]. The fastest MSM PD on crystalline silicon reported previously had a 4 μm single gap and an intrinsic 3 dB bandwidth of 22 GHz characterized using a continuous-wave laser heterodyne system [31].

In the paper, we discuss the fabrication and performance of nanoscale terahertz MSM PD’s on bulk and LT-GaAs and crystalline Si, which have a faster FWHM impulse response and a higher 3 dB bandwidth than previously reported. In the next section, we describe fabrication of MSM PD’s with nanoscale finger spacing and width using ultrahigh resolution electron-beam lithography. In Section III, we present Monte Carlo simulation of the impulse response of nanoscale MSM PD’s, analyze the roles of electrons and holes in the performance of the PD’s, explore the ultimate speed limit of transit-time-limited MSM PD’s, investigate the factors that are important to detector capacitance, and discuss scaling rules for achieving high-speed PD’s. In Section IV, we present experimental results of nanoscale terahertz MSM PD’s on bulk and LT-GaAs and crystalline silicon, and we compare them with modeling and scaling rules.

II. DETECTOR FABRICATION

Nanoscale interdigitated metal fingers were fabricated on semiconductor substrate by using electron beam lithography and a lift-off technique. The basic steps of the fabrication are illustrated in Fig. 2. First, polymethylmethacrylate (PMMA) was spun on the substrate. For MSM PD’s with finger spacing smaller than 50 nm, a single layer 70 nm PMMA was used; for finger spacing and width larger than 50 nm, double layer PMMA was used, with a 70 nm thick layer of 100 K molecular weight PMMA on the bottom and a 70 nm thick layer of 950 K molecular weight PMMA on the top. The double layer scheme was designed to achieve undercut in the resist for easier lift-off than that for single layer PMMA. Each layer of PMMA was baked at 160°C for over 12 h after spinning. Interdigitated line patterns were exposed in the resist using a custom-built high-resolution electron-beam lithography system converted from a JEOL-840 scanning electron microscope operated at 35 KV [32]. The exposed PMMA was developed in cellosove : methanol (3 : 7) developer. After exposure and development, metals (Ti–Au) were evaporated onto the samples and were lifted off in acetone. Fig. 3 shows scanning electron micrographs of (a) a MSM PD with 50 nm finger spacing and width and a detection area of 10 μm × 10 μm, and (b) a MSM PD with finger spacing and width of 25 nm.

For high-speed measurements, coplanar stripes of Ti–Au (50 nm/200 nm thick) with a linewidth of 16 μm and a spacing of 9 μm were fabricated on the substrate. The characteristic impedance of the stripes on GaAs substrate is 75 Ω.

Three different substrates were used for MSM PD’s: bulk GaAs, LT-GaAs, and crystalline silicon. Semiinsu-
Fig. 3. Scanning electron micrographs of MSM photodetectors of (a) 50 nm finger width and 50 nm finger spacing and (b) 25 nm spacing and 25 nm width. The metals are Ti–Au.

interdigitating (Si) GaAs had a carrier concentration of \( \sim 1.5 \times 10^{17} \text{ cm}^{-3} \), electron mobility of 6500 cm\(^2\)/V \cdot s, and a resistivity of \( 5 \times 10^7 \Omega \cdot \text{cm} \). The 1 \( \mu \)m thick L-T-GaAs was grown at 210°C on Si–GaAs with a growth rate of 0.5 \( \mu \)m/h and annealed at 600°C for 1 h. The Si substrate was on a p-type Si wafer with a doping concentration of \( 8 \times 10^{14} \text{ cm}^{-3} \).

Various epitaxial growth structures can be incorporated into the substrates for better detector performance. For example, a superlattice structure can be used to prevent carriers generated in the substrate from entering the detector active layer. A quarter-wave-stack reflector underneath the active layer plus an antireflection coating on the top surface can drastically improve the detectors’ sensitivity.

When the finger width becomes very narrow and metal thickness becomes thin, the resistance of a metal finger can be very high. Experimentally, we found that the actual resistance of a metal finger with a nanoscale width and thickness can be several times higher than theoretical value estimated from the bulk resistivities, because of large surface-to-volume ratio which makes the collisions between electrons and the metal boundary significant. To study the resistance of thin metal lines, we fabricated metal lines of 15 nm Ti and 35 nm Au on SiO\(_2\) substrate and measured their resistances [34]. The linewidth varies from 40 nm to 1.1 \( \mu \)m. The SiO\(_2\) substrate was used to reduce the leakage current between the contact pads, improving the measurement accuracy. Fig. 4(a) shows the experimental results of the resistances as well as theoretical values calculated from bulk resistivities of Au and Ti. The ratio of the measured resistance of the metal wires to the theoretical one, as plotted in Fig. 4(b), is about 3.7 when the linewidth is wider than 0.1 \( \mu \)m, and it is doubled to 7.6 when the linewidth is narrower than 0.1 \( \mu \)m. This suggests that the first factor of 3.7 increase in the measured resistance is due to the fact that the electron scat-
tering caused by the two boundaries in the direction of the metal thickness dominates the total resistance of metal fingers; and that for a metal finger of a linewidth less than 0.1 μm, another factor of 2 further increase in the measured resistance is due to the additional scattering caused by the two boundaries in the direction of metal width. Clearly, in order to reduce the total resistance of metal fingers for high-speed operation thicker metal electrodes and shorter finger lengths are preferred.

III. MODELING AND SCALING RULES

In order to design nanoscale terahertz MSM PD’s, understanding device operation is crucial. We used a Monte Carlo method to simulate the impulse response of transit-time-limited MSM PD’s and study the roles of electrons and holes, and we used a conformal mapping method to study how the device capacitance per unit finger length depends on the detector’s finger spacing and width. Through these studies, we have proposed rules for achieving high-speed MSM PD’s. These modeling and scaling rules have been used to design and understand the MSM PD’s presented in Section IV.

A. MONTE CARLO SIMULATION OF INTRINSIC RESPONSE OF MSM PD’s

A Monte Carlo simulation program has been developed for simulating the intrinsic impulse response of transit-time-limited MSM PD’s. The displacement current which dominates the impulse response is calculated by using an image charge method. Several assumptions were made in the simulation. 1) Electric field is one-dimensional and uniform in the semiconductor. 2) The optical pulsewidth is assumed to be a δ function, and the illumination is spatially uniform over the whole device. 3) Electrons are not injected from the metal into the semiconductor over the Schottky barrier. 4) Scatterings from phonons, electrons, and impurities are characterized by a single scattering time constant, which is chosen so that it is self-consistent with the measured parameters (electron and hole mobilities) of semiconductor samples. 5) The inductance, edge effects, and other parasitics are neglected. As discussed in Section IV, experimental data showed that the simplified 1-D simulation is a good approximation in estimating detector response time, even when the finger spacing is much smaller than the absorption length, and gives a good guidance in MSM PD design.

Fig. 5(a) shows the Monte Carlo simulation of the intrinsic current response of a GaAs MSM PD with 25 nm finger spacing and width, and the external current after introducing the capacitance of the detector and the load resistance (RC = 0.06 ps). The constant slopes in the intrinsic current profile are due to the assumption that light uniformly illuminates the entire device, which is more realistic than the assumption that the light is illuminated only in the middle of two fingers [39], [40].

For a 25 nm finger-spacing GaAs MSM PD, the FWHM of an intrinsic impulse response is 0.16 ps, and the FWHM of an extrinsic impulse response is 0.25 ps [Fig. 5(a)]. The curve in Fig. 5(b) separates the external impulse response due to the electrons from that due to the holes. This clearly shows that the current peak is due to the electrons and the long tail is due to holes, which move much slower than the electrons. Fourier transforms of the external responses with and without hole current are shown in Fig. 6. Including both electron and hole currents, 3 dB bandwidth of the detector is 400 GHz; when the hole current is eliminated, 3 dB bandwidth is over 1.8 THz. Therefore, for high-speed applications, one should try to reduce or eliminate the hole current.

In simulating external response time, the load resistance is assumed to be 50 Ω and the device capacitance of 1.2 fF, giving an RC time constant of 0.06 ps. It assumes that the active area of the detector of 25 nm finger spacing and width is 1 μm × 1 μm and the capacitance per finger length is 0.06 fF/μm. When the metal thickness is 50 nm (15 nm Ti and 35 nm Au), the total detector series resistance is 40 Ω, which is smaller that load resistance. How to calculate and reduce capacitance and resistance will be discussed later.

Fig. 7 shows the intrinsic response time versus the finger spacing of transit-time-limited MSM PD’s on GaAs.
and Si. Two things are very interesting. First, although the response time of a Si MSM PD is longer than that of GaAs due to heavier electron effective mass, the intrinsic response time of a Si MSM PD with a 25 nm finger spacing can reach 1 ps and the 3 dB bandwidth can be 440 GHz—very promising for application in Si-based optoelectronics. Second, when the finger spacing is less than 0.5 μm for GaAs MSM PD’s and 0.25 μm for Si MSM PD’s, the response time decreases much faster than that for a larger finger spacing. This is due to the fact that when the finger spacing becomes comparable to the mean free path, electrons encounter less scattering and travel more ballistically. In the simulation, we assumed that the maximum velocity of carriers is the saturation velocity, 2 × 10^7 cm/s for GaAs and 1 × 10^7 cm/s for Si. This certainly can be an underestimation for the MSM PD’s of sub-50 nm finger spacing, because of the electron velocity overshoot \[41, 42\]. The fast device operation due to carrier velocity overshoot is a unique feature of nanometer-finger-spacing MSM PD’s.

The response time of a transit-time-limited MSM PD is strongly affected by the biasing electric field. The response time of GaAs MSM PD’s with different finger spacing at different biasing fields is given in Fig. 8. At a low field, photogenerated carriers will move at a small drift velocity and give rise to a long transit time. This phenomenon distinguishes transit-time-limited PD’s from recombination-time-limited PD’s.

B. Calculation of Detector Capacitance

The RC time constant of a MSM PD can become the dominant limitation to its speed. Therefore, estimation of detector capacitance is necessary in detector design. Using a theoretical model based on conformal mappings, we calculated detector capacitance as a function of finger width and spacing as follows \[43\]:

\[
C_0 = \frac{\varepsilon_0 (1 + \varepsilon_s) K}{K'}
\]

where \(C_0\) is the capacitance per finger length, \(\varepsilon_0\) is dielectric constant of vacuum, \(\varepsilon_s\) is the relative effective dielectric constant of the semiconductor. \(K\) and \(K'\) are elliptic integrals defined as:

\[
K = K(k) = \int_0^{\pi/2} \frac{d\phi}{\sqrt{1 - k^2 \sin^2 \phi}}
\]

\[
K' = K(k'), \quad k' = \sqrt{1 - k^2}
\]

\[
k = \tan^2 \frac{\pi W}{4p}
\]

where \(w\) is the finger width and \(p\) is the finger pitch (i.e., sum of width and spacing). Fig. 9 shows the dependence of capacitance per unit finger length on the ratio of finger width to pitch. The total detector capacitance is given by:

\[
C = C_0 \times A/p
\]

where \(A\) is the detection area of the detector. This indicates that for a given detector area and finger pitch size, the smaller the finger width, the smaller the detector’s capacitance.

C. Calculation of Detector Resistance

The resistance of nanoscale MSM PD’s was calculated using a dc model, which assumes that total resistance \(R\) is equal to the sum of resistances of the two sets of fin-
Fig. 9. Capacitance per unit finger length of MSM photodetectors on GaAs and Si versus the ratio of finger width to the pitch (i.e., the sum of the spacing and the width).

where \( R_0 \) is the resistance per finger and \( N \) is the number of fingers on each side. The \( R_0 \) was calculated from the measured resistivity (such as the one given in Section II) multiplying by the finger geometry; therefore, it is very close to the real dc resistance of MSM PD. Although the resistance at high frequency may differ from that in dc due to skin depth effect, radiation loss, etc., we think that dc resistance is a good approximation for our nanoscale MSM PD’s, since the thickness of the metal finger is less than the penetration depth (which is 110 nm at 500 GHz for Au).

D. Scaling Rules

Through the experimental data, the simulation of impulse response, and the calculation of device capacitance, we can summarize general scaling rules for designing high-speed MSM PD’s. 1) For decreasing the intrinsic response time of transit-time-limited MSM PD’s, the finger spacing should be reduced. Nanoscale finger spacing also can further increase speed due to the ballistic transport, and it can achieve a very high electric field between the fingers at a low bias. 2) For reducing the MSM PD’s capacitance per unit finger length for a smaller \( RC \) time constant, the ratio of finger width to finger spacing has to be reduced. 3) For cutting off the tails in the impulse response of MSM PD’s, the hole current must be reduced or eliminated. 4) For reducing metal finger resistance, it is preferable to use shorter fingers and thicker metal.

IV. EXPERIMENTAL RESULTS

MSM PD’s with nanoscale finger spacing and width on bulk and LT-GaAs and crystalline Si were characterized in dc with a constant light source as well as in high frequencies using a high-speed electrooptical sampling system consisting of a 100 fs colliding-pulse mode-locked dye laser with a wavelength of 620 nm and a repetition rate of 100 MHz [44]. In the electrooptic sampling, a 100 fs laser pulse was split into two: one for exciting the MSM PD’s and the other for sampling the electrical signal generated by the exciting pulse [45]. The sampling utilizes the Pockel effect in a LiTaO\(_3\) tip placed 250 \( \mu \text{m} \) from the detectors. By changing the relative delay between the exciting pulse and the sampling pulse, the response of a MSM PD can be mapped out.

Some slower Si MSM PD’s were tested in a different way. A MSM PD was excited by a colliding-pulse modelocked dye laser with a pulse duration of \( \sim 250 \) fs and a wavelength of 620 nm, and the electrical signal from the detector was picked up by a probe station with 40 GHz bandwidth and was measured by a 50 GHz sampling oscilloscope [46].

A. MSM Photodetectors on Bulk and Low-Temperature Grown GaAs

The dc characteristics of MSM PD’s on bulk and LT-GaAs are similar, except that the LT-GaAs MSM PD’s usually have a smaller dark current. The detectors’ dark current is typically between 10 and 100 nA; however, it varies with different processing rounds due to different semiconductor surface conditions. Fig. 10 shows typical dc characteristics of a MSM PD on bulk GaAs, with a dark current of 40 nA at 0.5 V bias and sensitivity of 0.2 A/W at a wavelength of 632.8 nm. The current-voltage characteristics do not show perfect saturation because of the surface recombination centers. A passivation layer can be coated onto the devices to prevent surface oxidation and minimize the light reflection, therefore improving the dark current and sensitivity.

In high-speed measurement, MSM PD’s on LT-GaAs with different finger spacings and widths—100, 200, and 300 nm—were tested. We found that the detectors with 300 nm finger spacing and width have a FWHM of 0.87 ps (Fig. 11) and 3 dB bandwidth of 510 GHz [calculated using 0.443/(FWHM)] [47]. The bandwidth is consistent with direct Fourier transform of impulse response of the detectors, since there is no tail in the response. The impulse response time of the PD’s becomes progressively worse as the finger spacing and width become smaller. Furthermore, we varied the detector bias.
from 0.1 to 3 V and found that the FWHM does not depend on the bias, indicating that these nanoscale MSM PD’s on LT-GaAs are not transit-time-limited. Also, it should be pointed out that due to noise in the electrooptic sampling, there is a constant background in the sampling signal.

To understand the performance of these photodetectors, we tabulated the calculated intrinsic transit time, $RC$ time constant, measured FWHM, and 3 dB bandwidth of these detectors in Table I. The intrinsic time is defined as the FWHM of the intrinsic impulse response of a transit-time-limited detector and was calculated using one-dimensional Monte Carlo simulation. The detector capacitance was calculated using a conformal mapping method (Section III), which gives 0.06 fF/μm for a GaAs MSM PD with equal spacing and width. The $RC$ time constant of a PD is the product of detector capacitance and the impedance of the transmission line (75 Ω). The performance of metal fingers is not important in this case because it is smaller than the transmission line impedance. The $RC$ time constant multiplied by 0.69 gives the FWHM of the impulse response of a $RC$ circuit. The 3 dB bandwidth was obtained by the formula given above.

As shown in Table I, for 300 nm LT-GaAs MSM PD’s, the measured FWHM is shorter than both the intrinsic transit time and $RC$ time constant multiplied by 0.67; therefore, its speed is dominated by the recombination time of the LT-GaAs. Simulation using a transmission line model indicated that the 0.87 ps FWHM impulse response corresponds to a 0.2 ps recombination time of the LT-GaAs [48]. Subpicosecond recombination time in LT-GaAs is expected, since the excess arsenic in the LT-GaAs would precipitate during the annealing at 600°C to form densely packed recombination centers. On the other hand, for LT-GaAs MSM PD’s with finger spacing and width of 100 and 200 nm, the measured FWHM responses are longer than 0.87 ps—the response of recombination-time-limited PD’s, and longer than the intrinsic transit time, but are the same as 0.69 times of the $RC$ time. This implies that they are $RC$ time constant limited.

A MSM PD on bulk GaAs with 100 nm finger spacing and width was also tested. The impulse response has a FWHM of 1.5 ps (Fig. 12) and 3 dB bandwidth of 300 GHz. The fact that this FWHM of MSM PD on bulk GaAs is almost the same as that on LT-GaAs with the same device dimension indicates that the speed of this 100 nm MSM PD on bulk GaAs is limited by the $RC$ time. From Table I, it predicts that if the $RC$ time constant can be reduced to a value less than the intrinsic transit time, the speed and the 3 dB bandwidth of a 100 nm MSM PD on bulk GaAs can be increased by a factor of 4.

The major difference between the impulse response of MSM PDs on bulk GaAs and LT-GaAs, as shown in Figs. 11 and 12, is that the MSM PD’s on bulk GaAs have a longer tail. It is important to notice that the penetration depth of 620 nm light in GaAs is ~300 nm which is much deeper than the region where the electric field of a MSM PD of 100 nm finger spacing concentrates, and that the carrier lifetime in bulk GaAs is on order of tens of picoseconds. This implies that significant portion of photoexcited carriers are in the low field region and they may contribute significantly to the tail of impulse response of
the MSM PD. The details of effects of photogenerated carriers deep inside the semiconductor to the impulse response is under study.

We also found that MSM PD's on bulk GaAs are about a factor of two more sensitive than those on LT-GaAs. The oscillations in the measured response curve are believed to be caused by multiple reflection of detector signal in LiTaO₃ crystal tip [49].

B. MSM Photodetectors on Crystalline Si

Two types of Si MSM PD's were tested. The larger one had finger spacing of 1.2 μm, finger width of 0.8 μm, and a detection area of 20 μm × 20 μm. The smaller one had finger spacing of 100 nm and a detection area of 10μm × 10 μm. The RC time constants were estimated at 2 ps for the larger one and 2.2 ps for the smaller one.

Typical dc characteristics of Si MSM PD's are shown in Fig. 13. Compared with GaAs MSM PD's, the saturation of current indicates fewer recombination centers on the Si surface. In high-speed measurement, the larger Si MSM PD's were tested using the sampling oscilloscope and probe station system described earlier. The impulse response as a function of the bias for beam energy 0.2 pJ per pulse is shown in Fig. 14. The FWHM response decreases as the bias increases, indicating that the detector response is carrier transit-time limited. The saturation of the response time is due to the saturation velocity of carriers. The impulse response of the detector at 12.5 V bias is shown in Fig. 15, which has a measured rise time of 11 ps and a FWHM of 18.7 ps [46]. Taking into account the response time of the measuring system, the deconvolved rise time and FWHM are 6.3 and 14.3 ps, respectively. Fourier transform of the deconvolved impulse response indicates a 3 dB bandwidth of 32 GHz. The 14 ps FWHM is somewhat expected, since the average transit time for electrons with a saturation velocity of 1 × 10⁹ cm/s across a 1.2 μm distance is 12 ps. We found that the sensitivity of MSM PD's on Si is higher by about a factor of 2 than that on bulk GaAs, and by a factor of 4 than that on LT-GaAs.

The response has a tail that can be fitted onto an exponential function of a decay constant of 160 ps. Again, one might suspect that this tail comes from the diffusion of carriers generated in the low field region of the semiconductor, since the penetration depth for 620 nm light in
Si is \( \sim 3 \mu m \) and the lifetime of free carriers in crystalline Si is over 100 ns. However, the tail in the response of 100 nm finger spacing and width Si MSM PD, as will be described later, has a decay time \( \sim 30 \) ps, which seems inconsistent with the carrier diffusion model, because the model requires a similar decay constant for both detectors. This problem is still under investigation.

Smaller Si MSM PD’s with 100 nm finger spacing and width were tested using a 100 fs CPM laser and electrophotometric sampling system. The measured impulse response of the detector is shown in Fig. 16. The FWHM is 10.7 ps and the 3 dB bandwidth is 41 GHz [44]. The MSM PD not only has a long tail (decay time \( \sim 30 \) ps), but its FWHM is much longer than the estimated RC time constant and the carrier transit time. We suspect that this may be due to leakage at the edge of the detector, since the active region is not isolated and the laser beam may illuminate the area outside the detector. Investigation is still in progress. Nonetheless, to our knowledge, this is the fastest MSM PD on crystalline Si reported to date.

V. CONCLUSION

Metal-semiconductor-metal photodetectors (MSM PD’s) with finger spacing and width as small as 25 nm have been fabricated on bulk and LT-GaAs and crystalline Si using ultrahigh resolution electron-beam lithography. They are the smallest MSM PD’s fabricated using electron beam lithography to date.

Monte Carlo simulation was developed to understand the impulse response of MSM PD’s. Study showed that the intrinsic response time of a transit-time-limited MSM PD with 25 nm finger spacing and width are, respectively, 0.16 ps for GaAs and 1 ps for Si. Study indicates that by using nanoscale finger spacing the detector’s speed can be further increased due to ballistic transport and velocity overshoot. Furthermore, it showed that the current peak in the impulse response is due to the electrons, and the long tail is due to holes that move more slowly than the electrons. By eliminating the hole current, the 3 dB bandwidth of the external response of a MSM PD with 25 nm finger spacing and width on bulk GaAs can increase from 0.4 THz to 1.8 THz. The relation between the detector’s capacitance per unit length and the ratio of finger width to the finger spacing is calculated using conformal mapping method, which indicates that reducing the ratio gives smaller capacitance.

Based on the modeling, scaling rules for designing high speed MSM PD’s were proposed. 1) To decrease the intrinsic response time of transit-time-limited MSM PD’s, the finger spacing should be reduced. Nanoscale finger spacing also can further increase speed due to ballistic transport and can achieve a very high field at a low voltage. 2) The ratio of finger width to the finger spacing should be reduced for smaller MSM PD capacitance per unit finger length. 3) The hole current should be reduced or eliminated to cut off the tails in the impulse response of MSM PD’s. 4) It is preferable to use shorter fingers and thicker metal to reduce metal finger resistance.

High-speed characterization with a 100 fs pulsed laser showed that the fastest MSM PD’s on LT-GaAs have finger spacing and width of 300 nm, FWHM of 0.87 ps and 3 dB bandwidth of 0.51 THz, limited by the recombination time. The MSM PD’s with smaller finger spacing and width on LT-GaAs were slower than 0.87 ps, dominated by detector RC time constant. The fastest MSM on bulk GaAs has finger spacing and width of 100 nm, FWHM of 1.5 ps and 3 dB bandwidth of 0.3 THz, limited by the RC time constant. A MSM PD with finger spacing of 1.2 \( \mu m \) and finger width of 0.8 \( \mu m \) on has FWHM of 14.3 ps and 3 dB bandwidth of 32 GHz, limited by the carrier transit time between the metal fingers. And the fastest MSM PDs on crystalline Si has finger spacing and width of 100 nm, FWHM of 10.7 ps, and 3 dB bandwidth of 41 GHz, much slower than expected. The reasons are unclear at this stage. To our knowledge, these detectors are the fastest nanoscale MSM PD’s of their kinds reported to date.

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