

## Jonathan Chang

Ph.D. Candidate  
Expected Graduation Date: August 2009

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### EDUCATION

**Princeton University** (9/03 – Present) M.A. Electrical and Computer Engineering. PhD in progress. GPA 3.8  
**California Institute of Technology** (9/99 – 6/03) B.S. Electrical and Computer Engineering with Honors. GPA 3.8

### SELECTED PUBLICATIONS

**Connections between the Lines: Augmenting Social Networks from Text** Jonathan Chang, Jordan Boyd-Graber, David Blei.  
To appear: *Knowledge Discovery and Data Mining*, June 2009.

**Mixtures-of-Clusterings by Boosting** Jonathan Chang, David Blei. *Snowbird Learning Workshop*, April 2009.

**Relational Topic Models for Document Networks** Jonathan Chang, David Blei. *Artificial Intelligence and Statistics*, April 2009.

**PU-BCD: Exponential Family Models for the Coarse- and Fine-Grained All-Words Tasks** Jonathan Chang, Miroslav Dudik, David Blei. *Proceedings of the Fourth International Workshop on Semantic Evaluations (SemEval-2007)*, June 2007.

**Automatic Instruction-Level Software-Only Recovery Methods** George A. Reis, Jonathan Chang, and David I. August. *IEEE Micro Top Picks Issue, Volume 27, Number 1*, January 2007.

**Non-Uniform Fault Tolerance** Jonathan Chang, George A. Reis, and David I. August. *Proceedings of the 2nd Workshop on Architectural Reliability (WAR)*, December 2006.

**Automatic Instruction-Level Software-Only Recovery Methods** Jonathan Chang, George A. Reis, and David I. August. *Proceedings of the International Conference on Dependable Systems and Networks (DSN)*, June 2006. **Winner William C. Carter Award.**

**Software-Controlled Fault Tolerance** George A. Reis, Jonathan Chang, Neil Vachharajani, Ram Rangan, David I. August, and Shubhendu S. Mukherjee. *ACM Transactions on Architecture and Code Optimization (TACO)*, December 2005.

**Design and Evaluation of Hybrid Fault-Detection Systems** George A. Reis, Jonathan Chang, Neil Vachharajani, Ram Rangan, David I. August, and Shubhendu S. Mukherjee. *Proceedings of the 32nd International Symposium on Computer Architecture*, June 2005.

**SWIFT: Software Implemented Fault Tolerance** George A. Reis, Jonathan Chang, Neil Vachharajani, Ram Rangan, and David I. August. *Proceedings of the Third International Symposium on Code Generation and Optimization*, March 2005. **Winner Best Paper Award.**

### TEACHING EXPERIENCE

**COS 424** (2/07 – 5/07) TA for an upper-level data analysis course.

**COS 402** (9/06 – 1/07) TA for an introductory machine learning course.

**ELE 375/COS 471** (9/04 – 1/05 & 9/05 – 1/06) TA for an upper-level computer architecture course.

### INTERNSHIPS

**Google** (6/07 – 9/07) New York, NY. Performed research on determining location relevance for the Books: Mapped project.

**INRIA** (6/06 – 9/06) Orsay, France. Performed research in and developed novel modular simulation methodologies for simulating processor architectures.

**nVidia** (6/02 – 9/02) Santa Clara, CA. Hardware engineering intern. Designed and tested portions of production graphics processor.

### HONORS AND AWARDS

Tau Beta Pi

Wu Prize for Excellence