16.4 A Flexible EEG Acquisition and Biomarker Extraction System Based on Thin-Film Electronics

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EEG is an important modality for many medical purposes. However, the low-amplitude of signals (10-to-100μV) and large number of channels (~20) raise numerous challenges, including electrode setup (correct placement, skin preparation, sanitation), patient comfort (number of channels, skin abrasion), and robust acquisition (electrode/wire motion artifacts, wire stray coupling). The recent emergence of low-cost, single-use, flexible, pre-gelled electrode arrays, as in Fig. 16.4.1, delivers significant advantages [1]. Today, these are passive, requiring connection to external readout electronics via a many-channel cable. We present the system in Fig. 16.4.1, having similar flexible form factor, but with the following enhancements: (1) embedded low-noise chopper-stabilized amplifiers implemented using amorphous-silicon (a-Si) thin-film transistors (TFTs) compatible with flexible substrates (i.e. low-temperature-processed, <180°C); (2) compressive-sensing detection and multiplexing of ~20 EEG channels onto a single interface using TFT scanning circuits, to substantially ease connection with an embedded IC; and (3) an algorithm whereby spectral-energy features, a generic EEG biomarker, are derived directly from the compressed signals (by a conventional CMOS IC). Seizure detection from the extracted features is demonstrated via analog replay of patient EEG through the system.

Previous instrumentation amplifiers using low-temperature-processed TFTs have focused on signals such as EMG [2], and do not achieve noise levels needed for EEG (which has significantly lower amplitude). Figure 16.4.2 shows the measured input-referred noise PSD of an a-Si amplifier, with biasing of 200μA. The band of interest for EEG (0-to-300Hz) is limited by 1/f noise, which can be high for low-temperature-processed TFTs due to high carrier-trap density in the semiconductor and semiconductor-dielectric interface [3]. Typically, 1/f noise is reduced by increasing device W and L. Though effective for TFTs, this worsens reliability, as shown in Fig. 16.4.3, due to the mismatch in TFTs, expected to improve with process refinement. Successful EEG acquisition is shown, by recording α waves from a standard Ag/AgCl electrode positioned at the occipital location (evoked by 10sec periods of closing/opening the eyes). The power drawn by the amplifier is 11mW from a 5V supply.

Acknowledgements: Funding provided by NSF (grant ECCS-1202168) and Systems on Nanoscale Information fabricS (SONIC), one of six STARnet Centers, sponsored by MARCO and DARPA.

References:

The implementation employs a 5kHz digital chopping signal CHOP distributed to all amplifiers, from a conventional CMOS IC through a TFT level converter [4]. The TFT sizes, stage biasing currents, and load resistances are as shown in Fig. 16.4.3, yielding a stage gain >10. Following the output modulator, a single-pole passive low-pass filter with cut-off frequency of 500Hz is implemented.

Following amplification, signals are multiplexed for subsequent readout and processing by a conventional CMOS IC. This reduces the number of bonds to the IC, which significantly benefits assembly cost and reliability on flexible substrates. Multiplexing is achieved using the TFT scanning circuit presented in [4], whereby channels are selectively enabled by enabling analog TFT switches in serial and without affecting the speed of the TFT scanning circuit, limited to ~10kHz. Given their frequency content following single-pole filtering, Nyquist sampling would limit the number of channels in the system to <5. However, as EEG is known to be sparse in the Gabor basis, the scanning implementation shown in Fig. 16.4.4 is used, employing an architecture that exploits compressive sensing [5]. The architecture consists of modulation (using TFT switches) with a 500Hz pseudorandom ±1 digital chopping sequence CHIP (distributed to all channels from a conventional CMOS IC through a TFT level converter [4]), followed by low-pass filtering with cutoff frequency <0.03Hz, to approximate integration. For demonstration, patient EEG from the CHB-MIT dataset, sampled at 256Hz, is relayed through the system by appropriately scaling the output of a 16b DAC. Figure 16.4.4 shows waveforms reconstructed from samples acquired by the system at 1-to-8 below the Nyquist rate.

Though the original EEG can be obtained in this way, full sparse reconstruction is computationally intensive. In many applications, extracting spectral-energy features is the primary interest. We present an approach, extending our previous algorithmic work [6], to obtain these efficiently from the compressed signals. As shown in Fig. 16.4.5, using the N-point vector x to represent EEG time samples in an epoch, a band-pass filtered signal y is obtained by multiplication with a matrix y = Hx. The M-point compressed EEG x̂ provides an underdetermined system of linear equations for recovering the underlying sparse signal when M=N (i.e. is the MxN compressive-sensing measurement matrix corresponding to the analog scanning system). Nonetheless, the error from linear estimation is tolerable as (1) the underlying sparse signal is in the Gabor domain, and so the signal-carrying elements, will in many cases be attenuated following band-pass filtering; and (2) since our interest is ultimately in the energy given by ||y||2, the error accumulated over all elements has diminished variance relative to the energy. In fact, in addition to linear estimation, further energy saving is possible by seeking random measurements of y rather than y itself. Since y from band-pass filtering is at least as sparse as x in the Gabor basis, a K-row compressive matrix H (with K=M whose elements are drawn from a Gaussian distribution, will present the property α(||y||2) = (||x||2), with some bound error (restricted isometry property). Thus, the desired spectral energy ||y||2 can be estimated from ||x||2. This allows us to solve for a compressed K×M transform H such that Hox = Θx (e.g. by finding a minimum-norm solution to Θx = 0x), for x in Fig. 16.4.5 compares estimates from 20 epochs (of 256sec length) for an EEG channel (10/10 non-/seizure) after filtering (center at 0Hz, bandwidth of 3Hz). A seizure detector is implemented by feeding 8 such features (filter centers 0-21Hz) from 7 EEG channels to a SVM classifier. True-pos./true-neg./error rates (TP/TN/Err) vs. compression are shown in Fig. 16.4.5, using 4950/100 non-/seizure epochs of EEG randomly sampled from one patient in the CHB-MIT dataset (replayed via a 16b DAC); direct classification results TP/TN/Err are shown, since sampling prevents mapping to latency/sensitivity/false-alarms.

The EEG acquisition system on flex is fabricated with a-Si TFTs processed in house at temperatures <180°C (Fig. 16.4.7). Figure 16.4.6 presents a measurement summary. Chopper stabilization reduces input-referred noise in the band of interest to 230nV/√Hz, and increases the CMRR to above 40dB (limited by mismatch in TFTs, expected to improve with process refinement). Successful EEG acquisition is shown, by recording α waves from a standard Ag/AgCl electrode positioned at the occipital location (evoked by 10sec periods of closing/opening the eyes). The power drawn by the amplifier is 11mW from a 5V supply.
**Figure 16.4.1:** System architecture enhancing current passive flex EEG electrode arrays [1], by introducing TFT amplifiers and acquisition circuits on flex, and a spectral feature-extraction algorithm.

**Figure 16.4.2:** Measured 1/f-limited noise PSD of an a-Si TFT amplifier, and TFT W, L sizing effect on 1/f noise and gate dielectric reliability.

**Figure 16.4.3:** Chopper-stabilized a-Si TFT low-noise amplifier and \( g_m/I_D \) analysis, showing \( f_c \) required for up-modulation.

**Figure 16.4.4:** TFT-based compressive-sensing scanning acquisition, and reconstructed waveforms measured by analog replay of EEG from CHB-MIT dataset through the prototype.

**Figure 16.4.5:** Spectral feature extraction from compressed signals (top); comparison of derived features from different compression levels (middle); and seizure detection results (bottom).

**Figure 16.4.6:** Measured performance summary and EEG acquisition of \( \alpha \) wave from subject using Ag/AgCl electrodes.
Figure 16.4.7: Prototypes on flexible substrates; all a-Si TFT circuits are fabricated in-house at temperature <180°C on 50μm-thick polyimide foil.